

Docket No.: 50389-048

**PATENT**

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

In re Application of	:	Customer Number: 20277
	:	
Hirohisa SAITO, et al.	:	Confirmation Number:
	:	
Serial No.:	:	Group Art Unit:
	:	
Filed: December 02, 2003	:	Examiner: Unknown
	:	
For: PACKAGE FOR HOUSING SEMICONDUCTOR CHIP, FABRICATION METHOD THEREOF AND SEMICONDUCTOR DEVICE		

**INFORMATION DISCLOSURE STATEMENT**

Mail Stop IDS  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.


The relevance of the references listed on attached Form 1449 is discussed in the present specification.

**Serial No.:**

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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**Date: December 2, 2003**

INFORMATION DISCLOSURE CITATION IN AN APPLICATION						ATTY. DOCKET NO. <b>50389-048</b>		SERIAL NO.				
(PTO-1449)						APPLICANT <b>Hirohisa SAITO, et al.</b>						
						FILING DATE <b>December 02, 2003</b>		GROUP				
U.S. PATENT DOCUMENTS												
EXAMINER'S INITIALS		CITE NO.	Document Number Number-Kind Code <sup>2</sup> (if known)		Publication Date MM-DD-YYYY		Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines, Where Relevant Passages or Relevant Figures Appear			
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FOREIGN PATENT DOCUMENTS												
EXAMINER'S INITIALS		CITE NO.	Foreign Patent Document Country Codes - Number - Kind Codes (if known)		Publication Date MM-DD-YYYY		Name of Patentee or Applicant of Cited Document		Pages, Columns, Lines Where Relevant Figures Appear		Translation	
											Yes	No
				JP 4-65544		10/20/1992	SUMITOMO ELECTRIC IND LTD			(Japan w/English Abstract)		
				JP P2002-121639A		04/26/2002	SUMITOMO ELECTRIC IND LTD			(Japan w/English Abstract)		
				JP P2001-244357A		09/07/2001	SUMITOMO ELECTRIC IND LTD			(Japan w/English Abstract)		
				JP P2002-252299A		09/06/2002	KYOCERA CORP			(Japan w/English Abstract)		
OTHER ART (Including Author, Title, Date, Pertinent Pages, Etc.)												
EXAMINER'S INITIALS		CITE NO.	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.									
EXAMINER						DATE CONSIDERED						

\*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

1 Applicant's unique citation designation number (optional). 2 Applicant is to place a check mark here if English language Translation is attached.